

Fig. 1 Prior Art

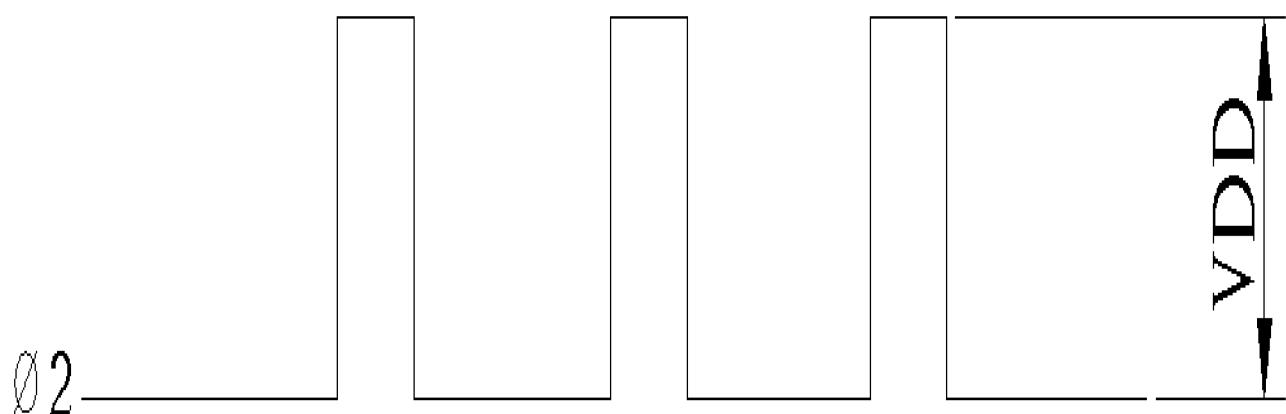
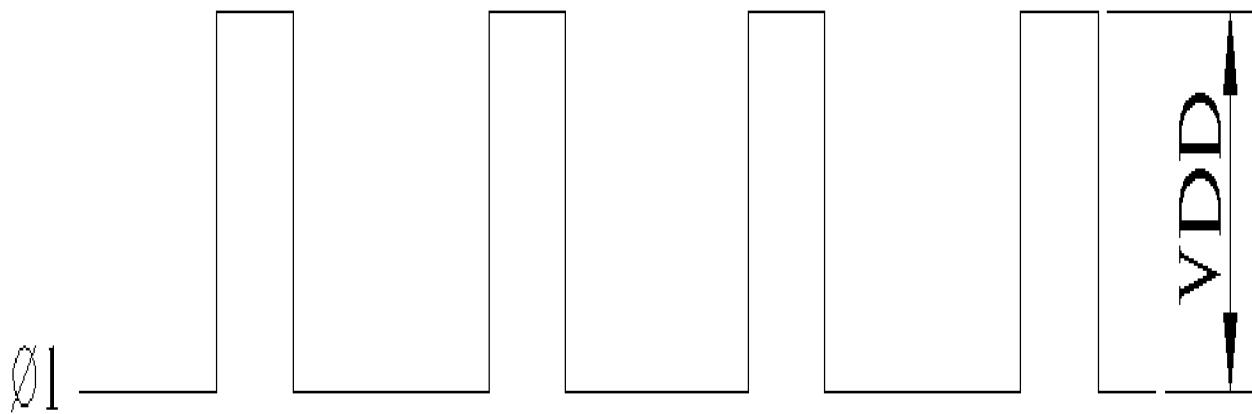


Fig. 2 Prior Art

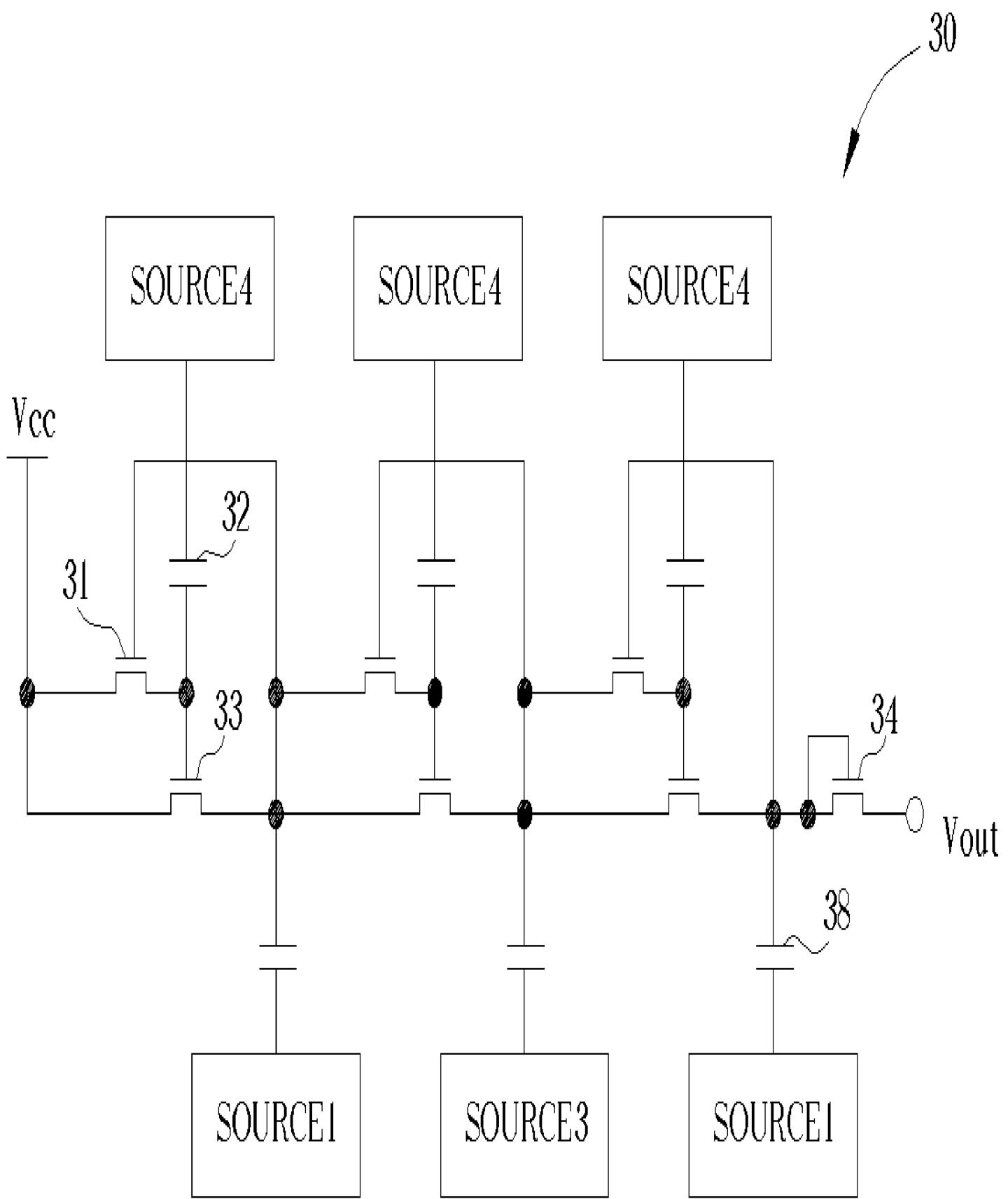


Fig. 3 Prior Art

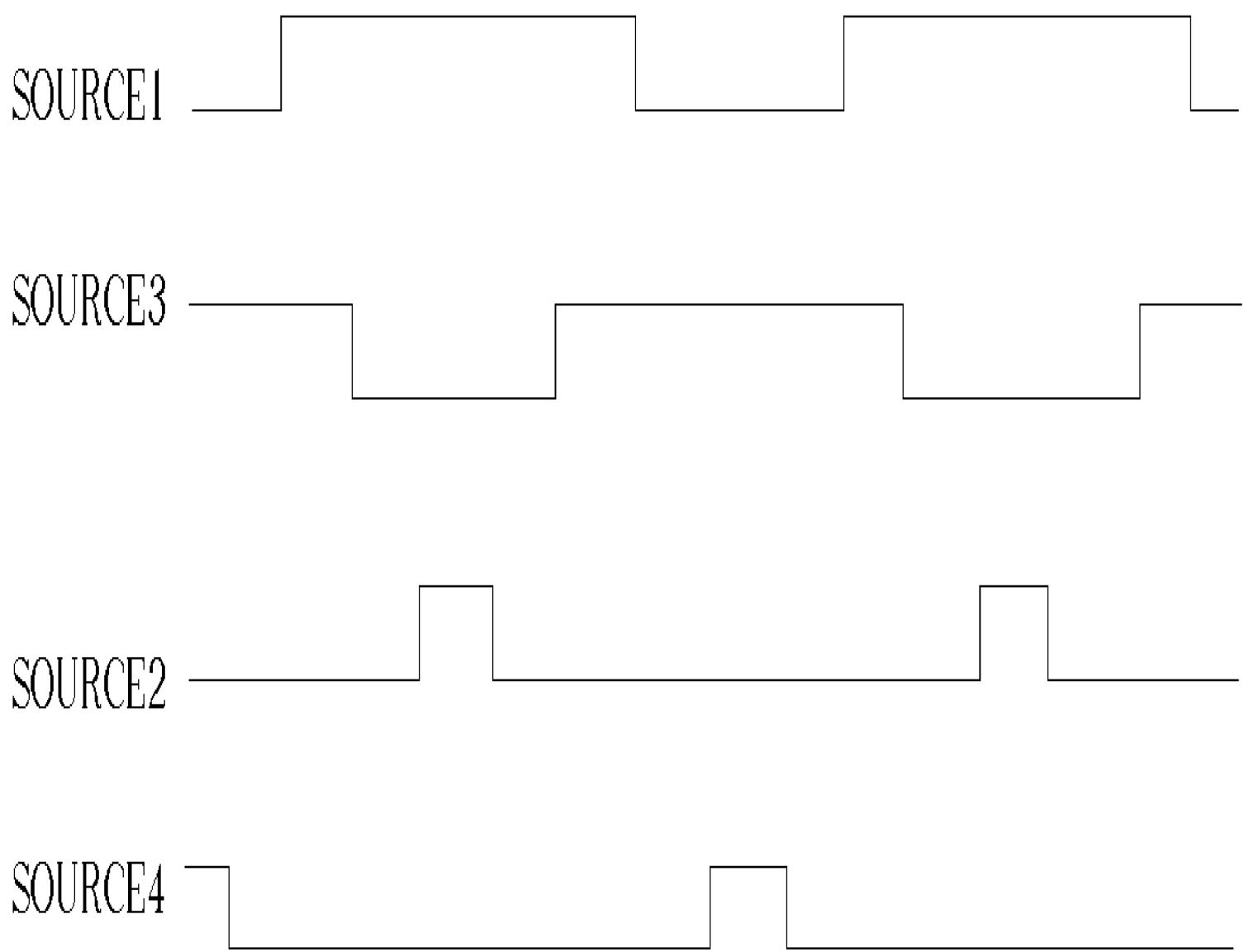


Fig. 4 Prior Art

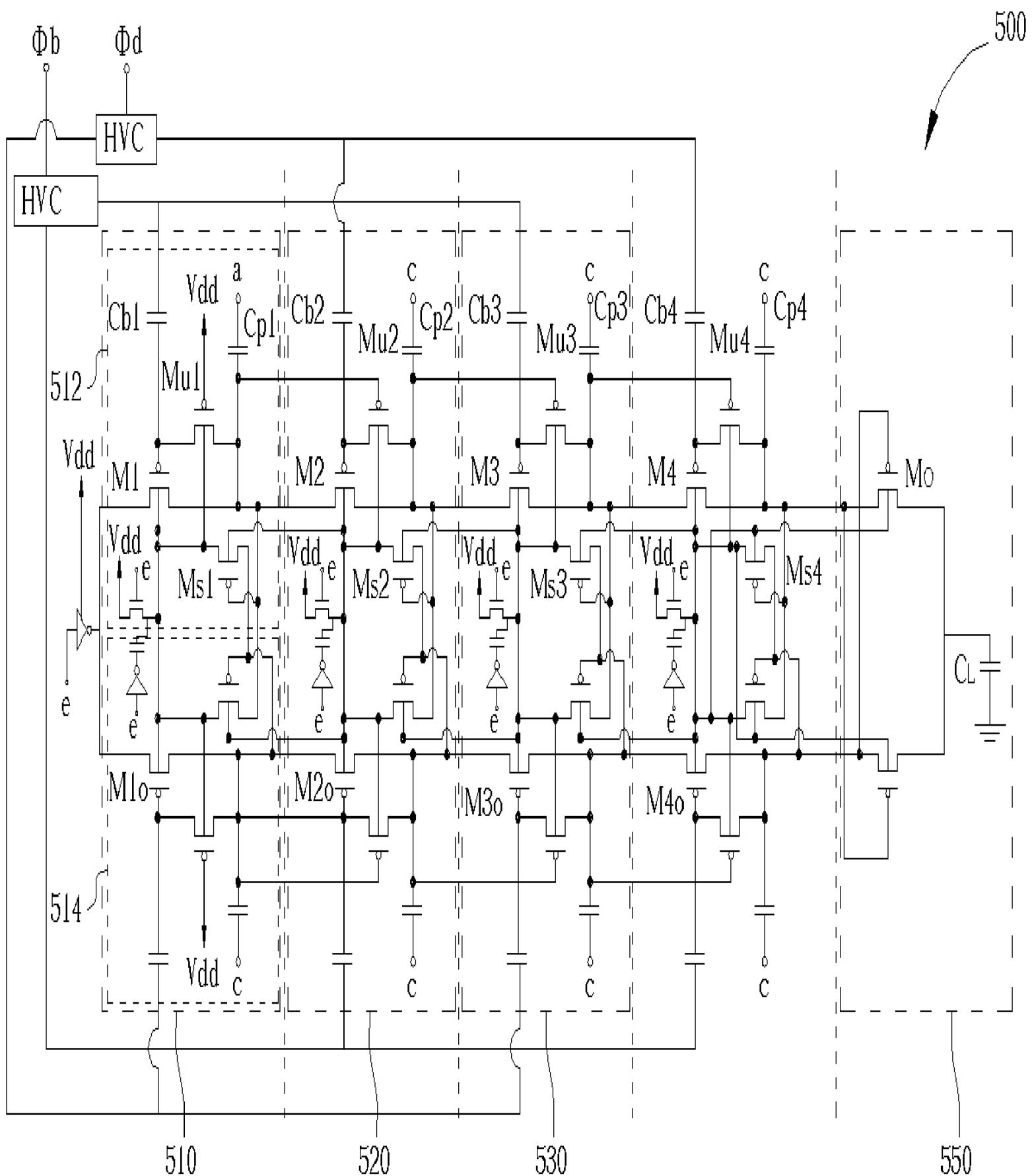


Fig. 5

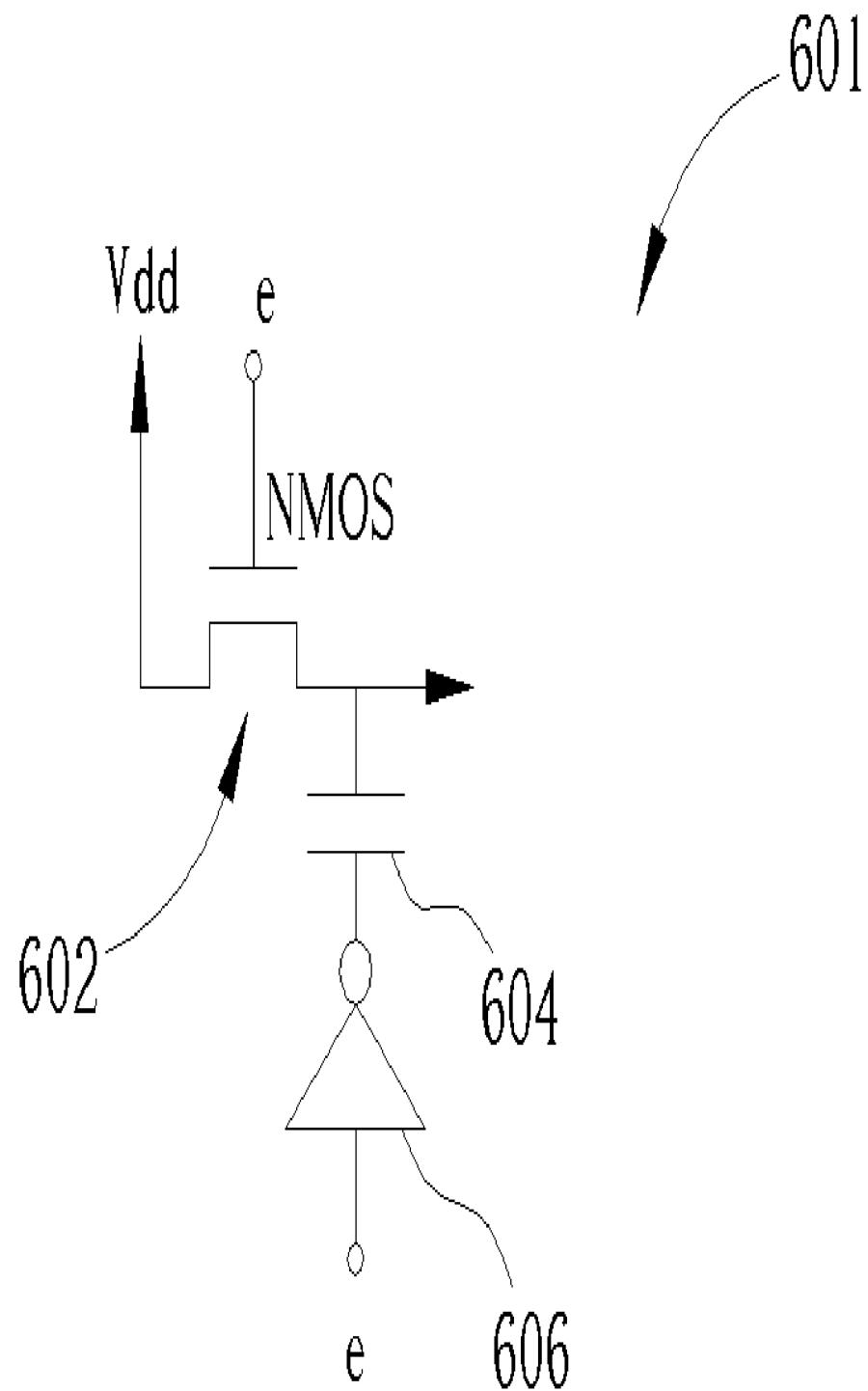


Fig. 6

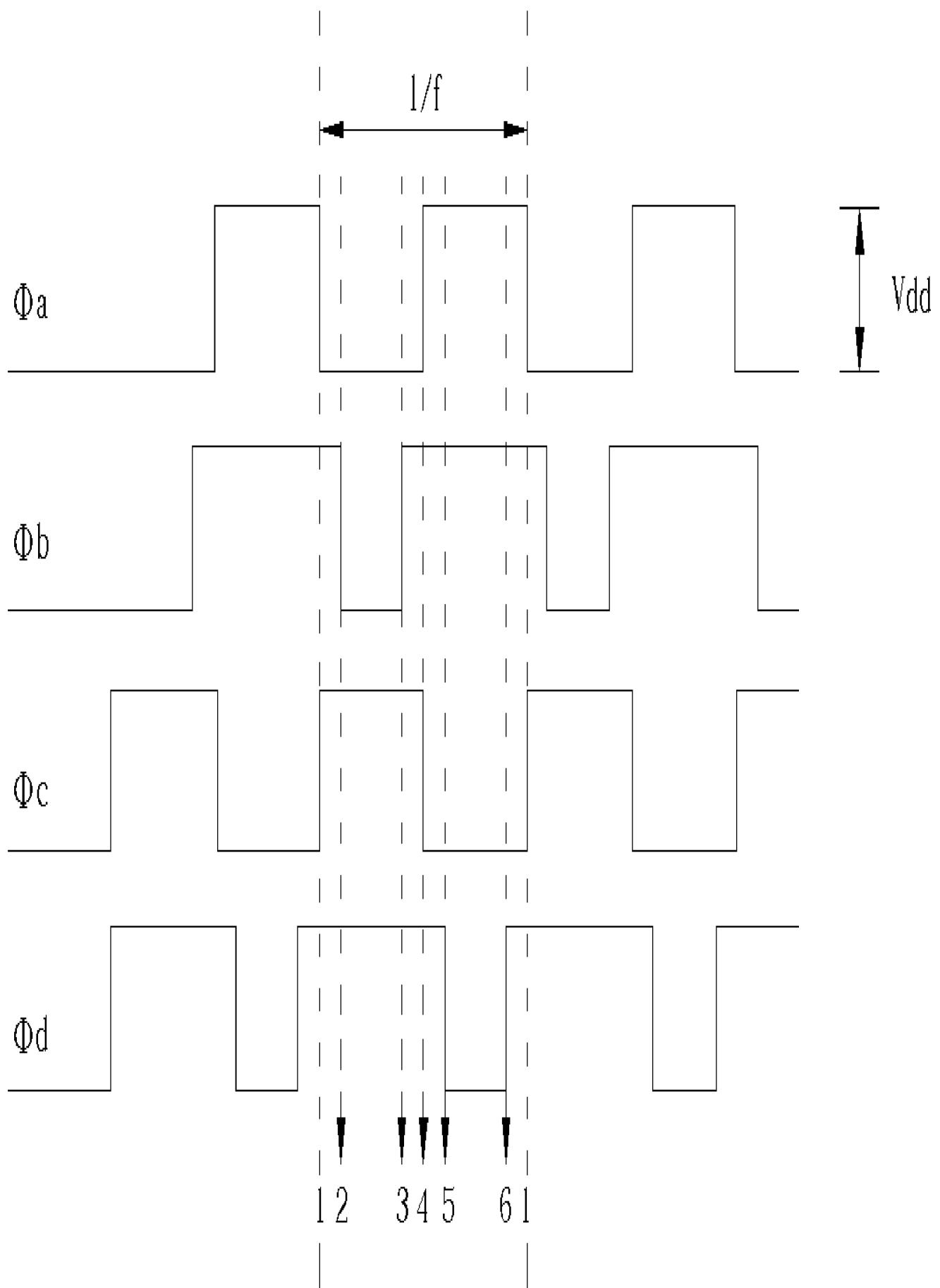


Fig. 7

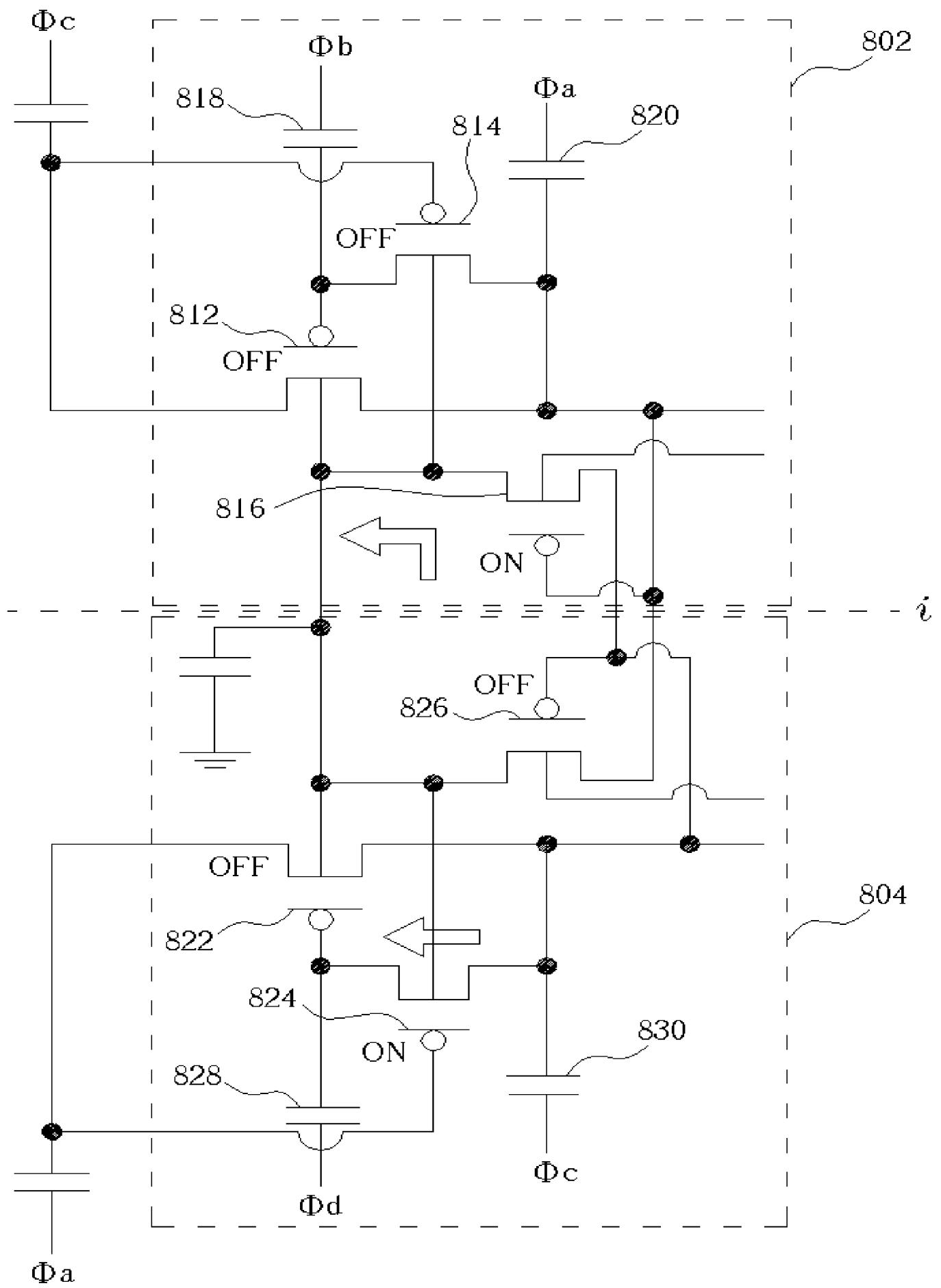


Fig. 8

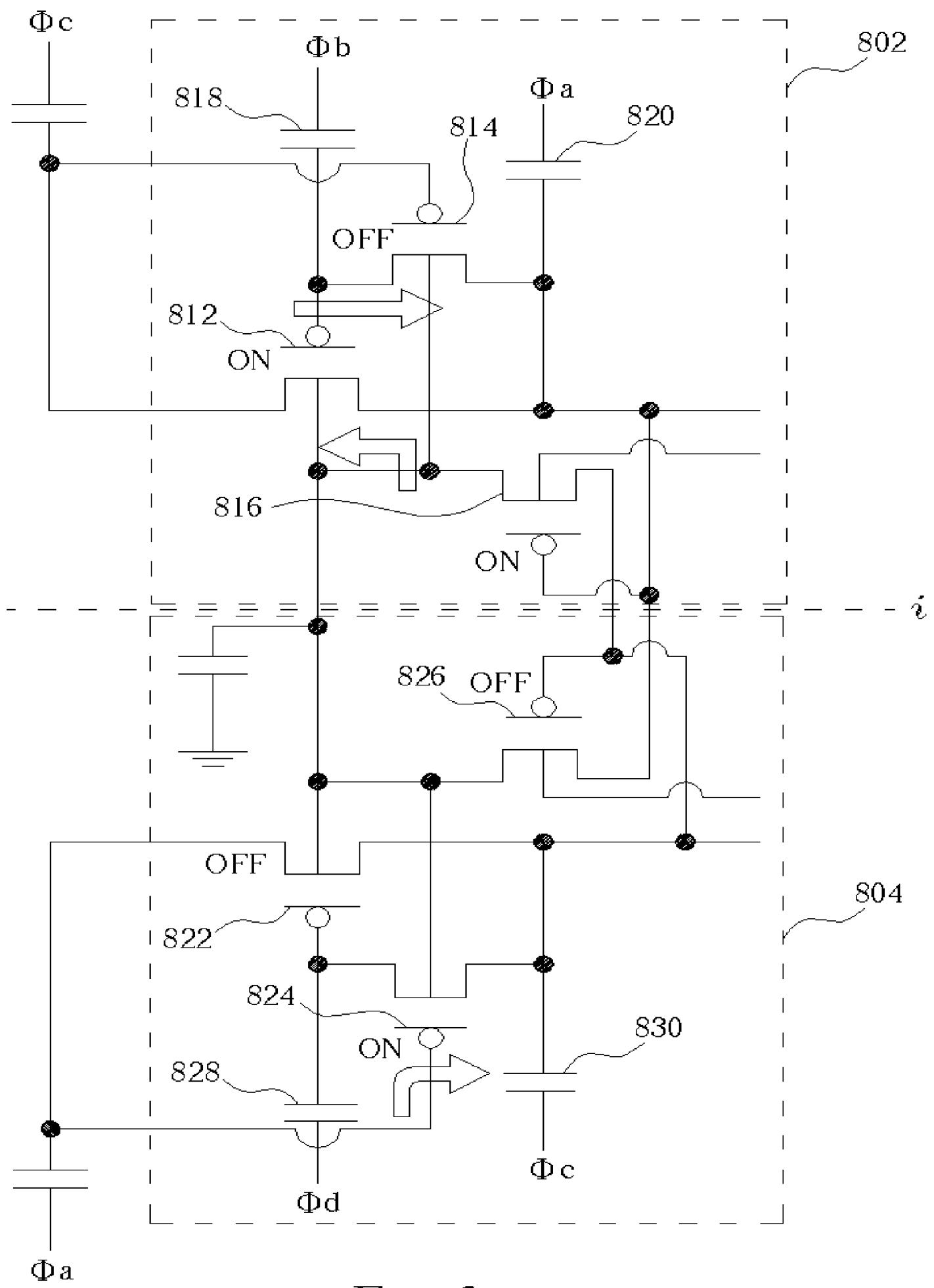


Fig. 9

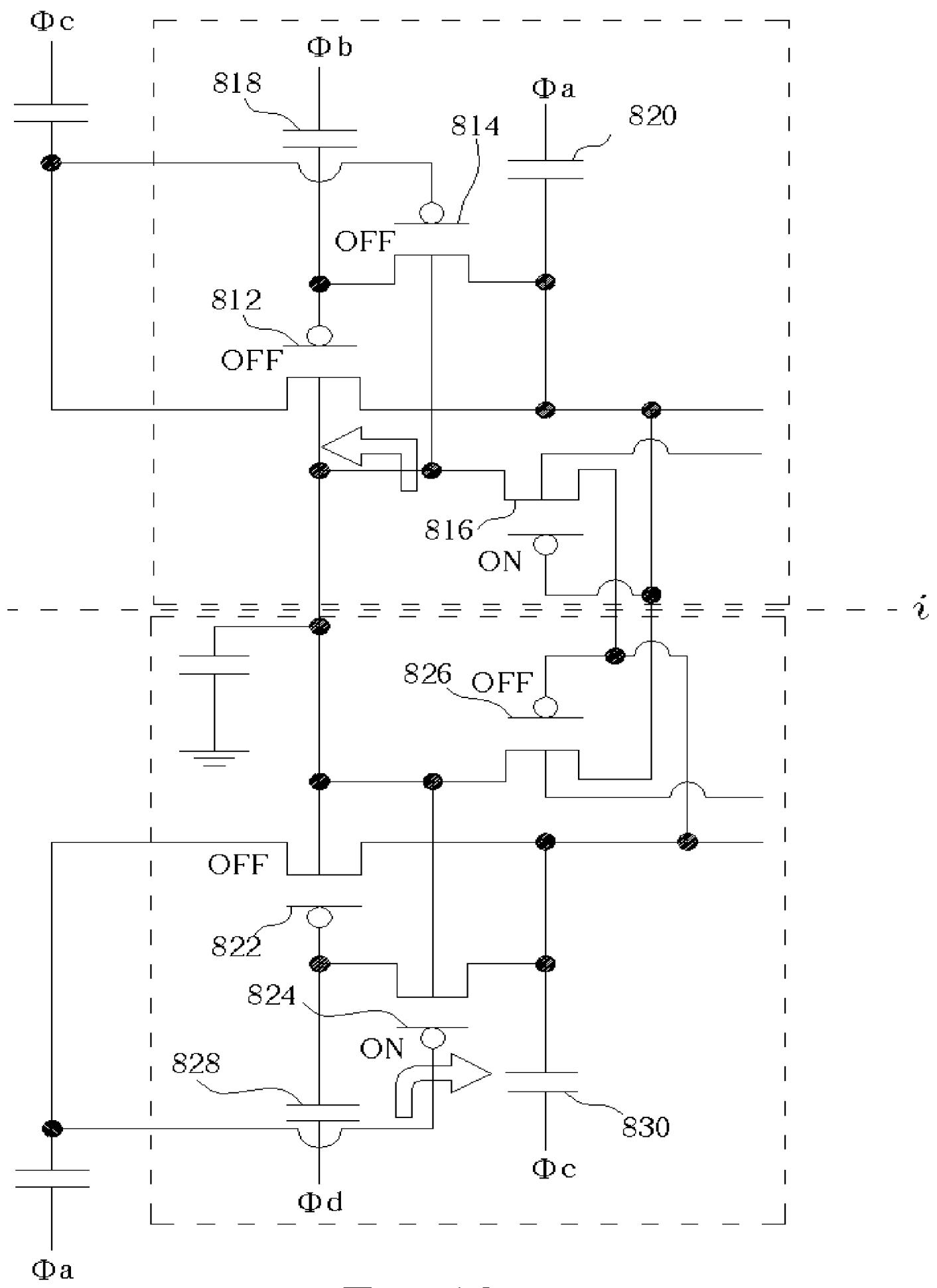


Fig. 10

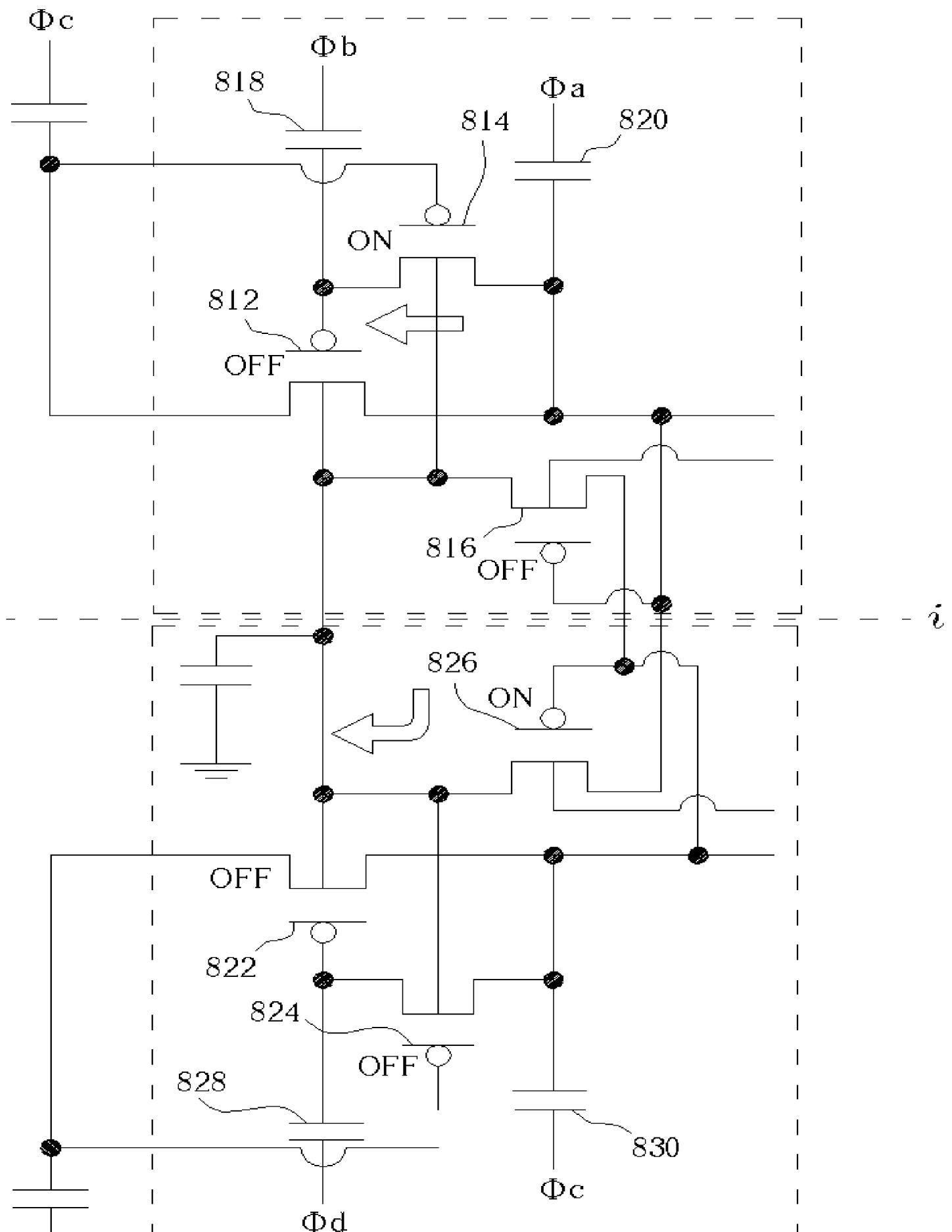


Fig. 11

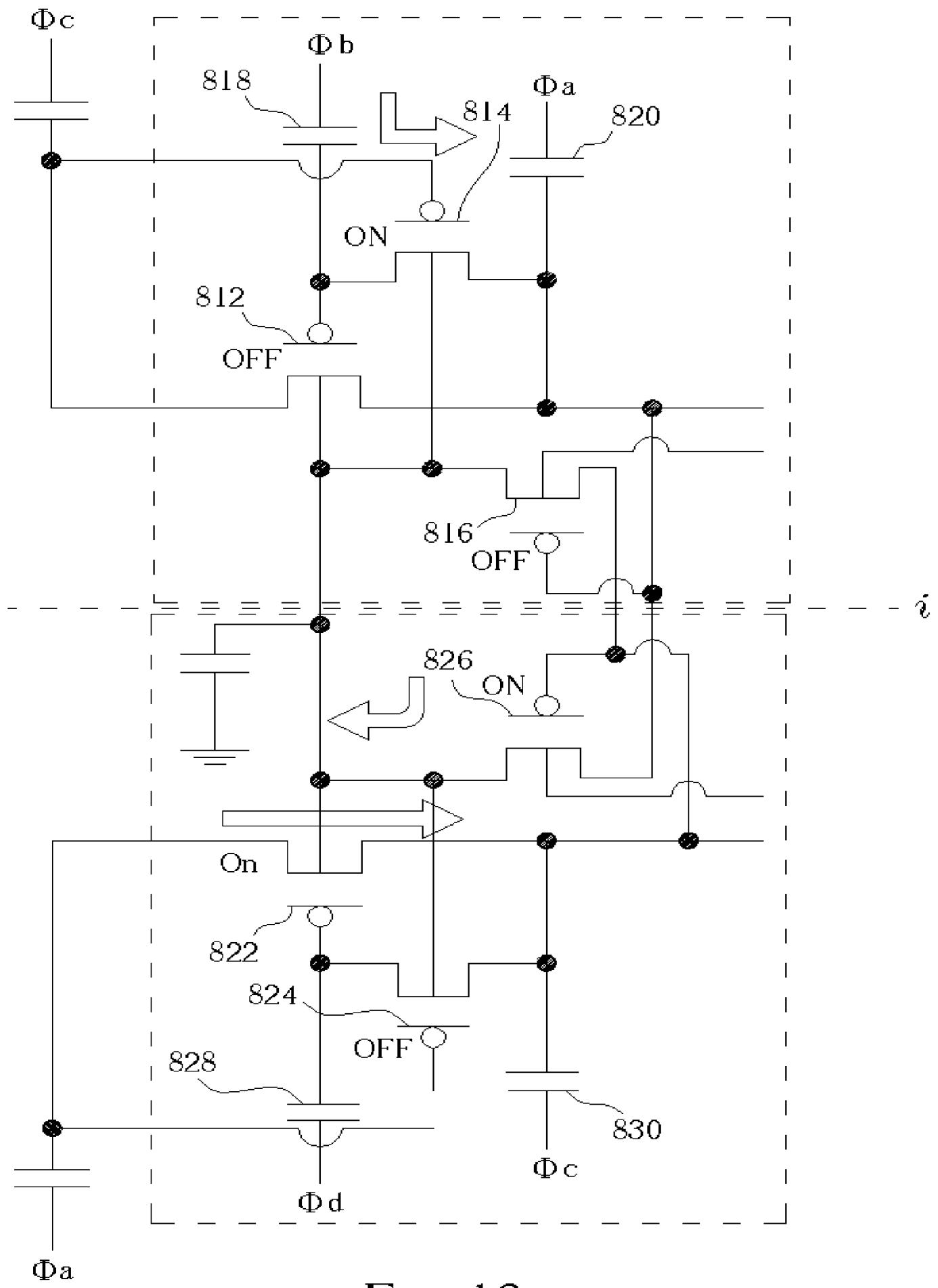


Fig. 12

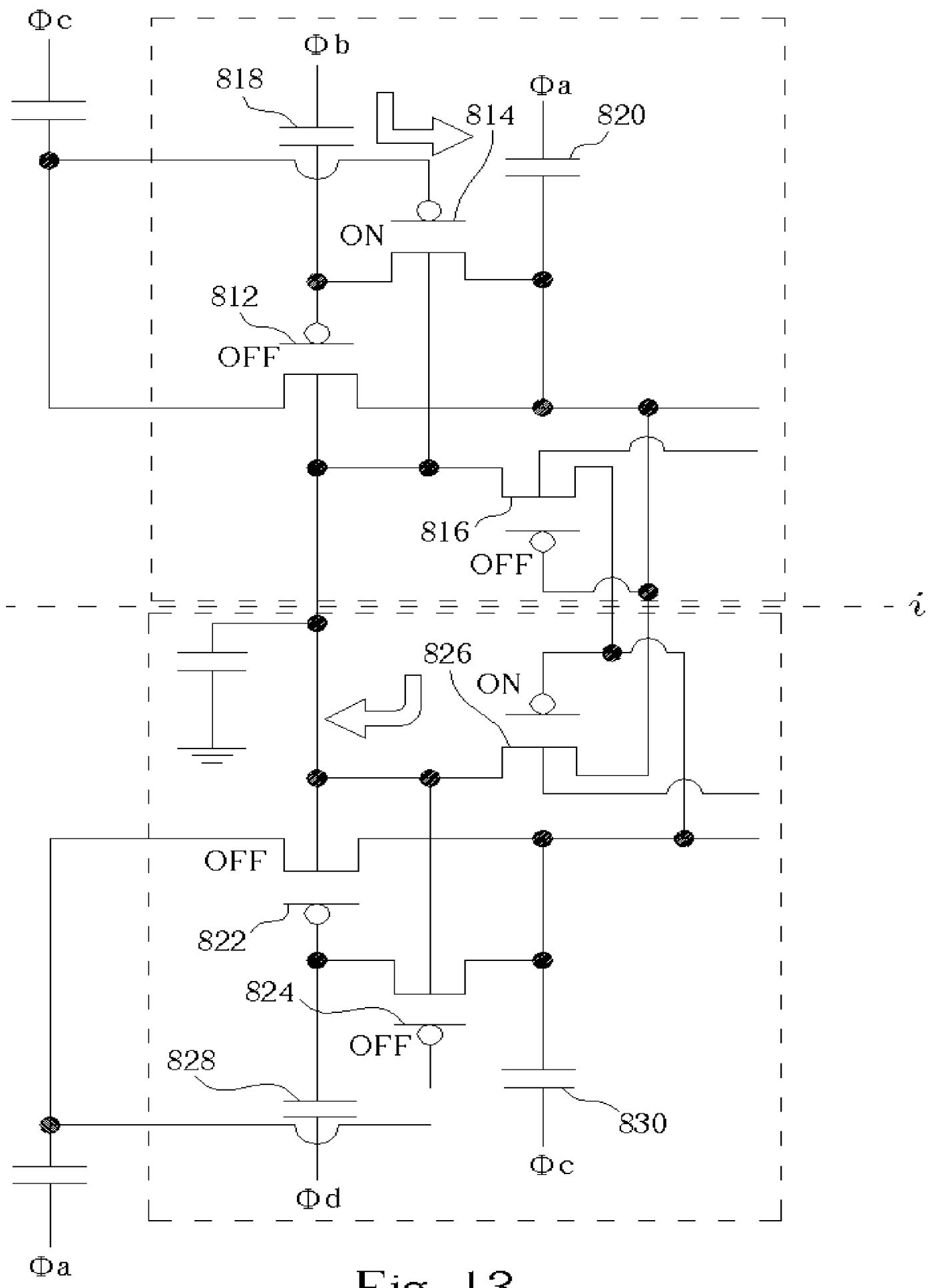


Fig. 13

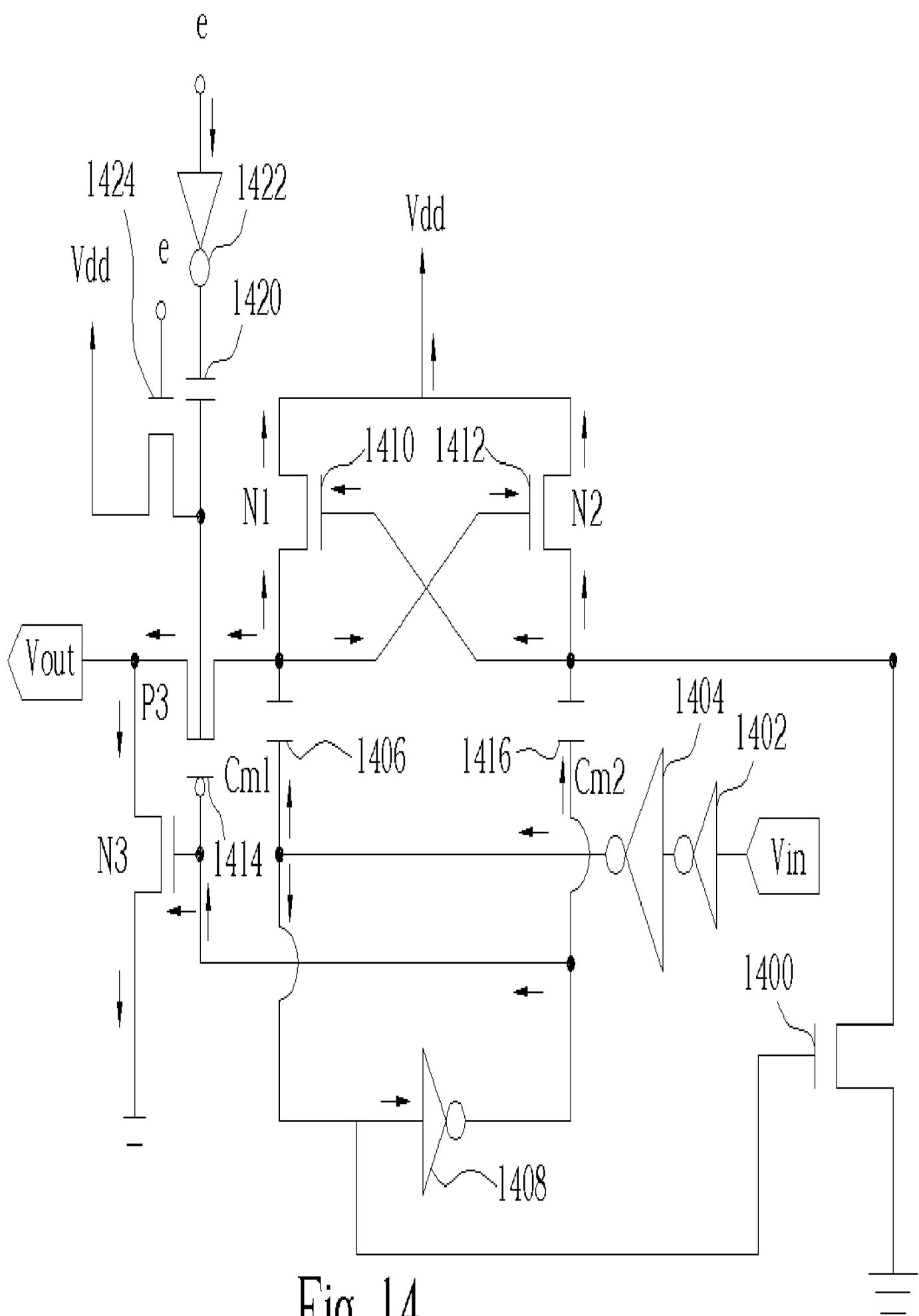


Fig. 14

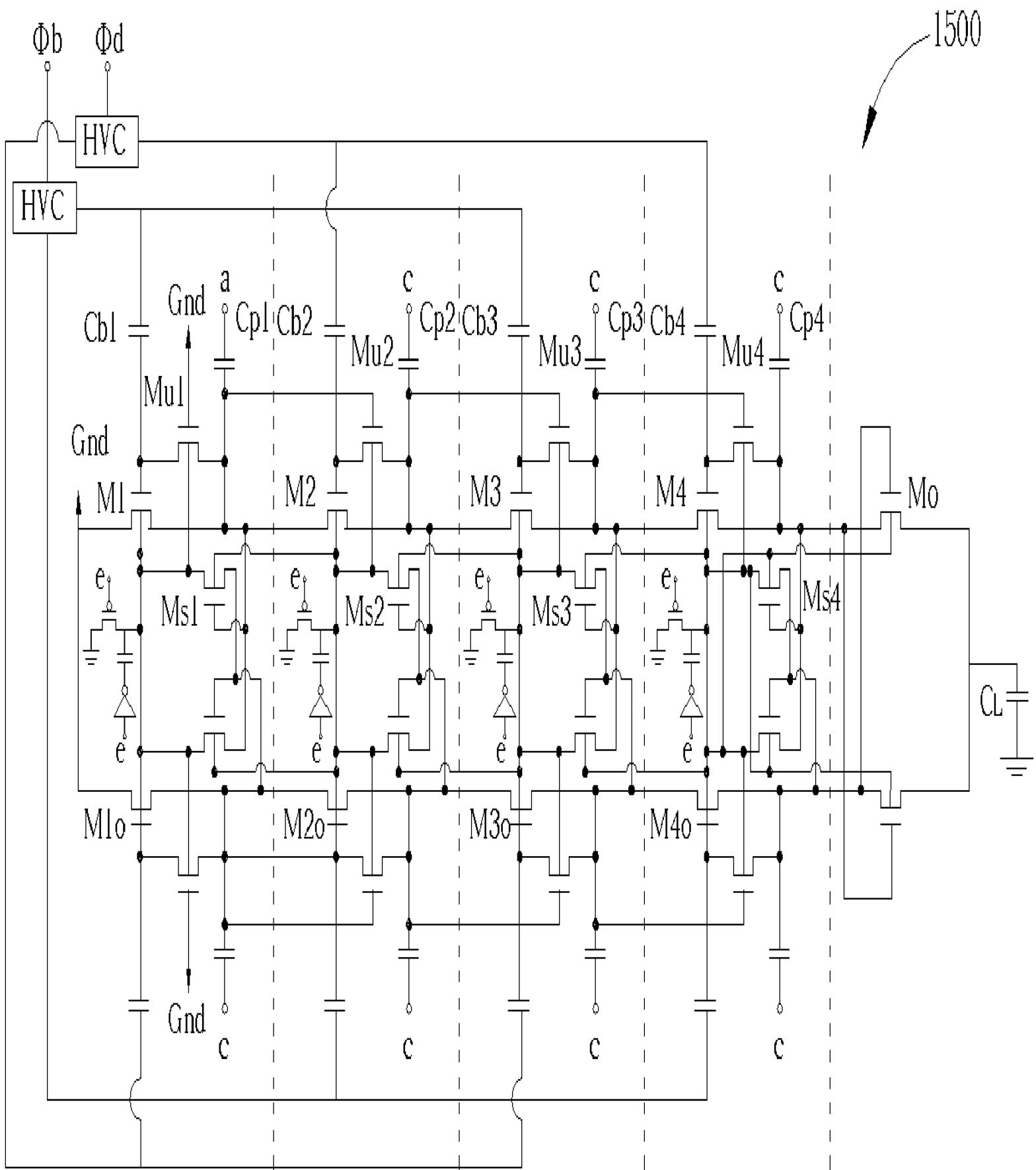


Fig. 15

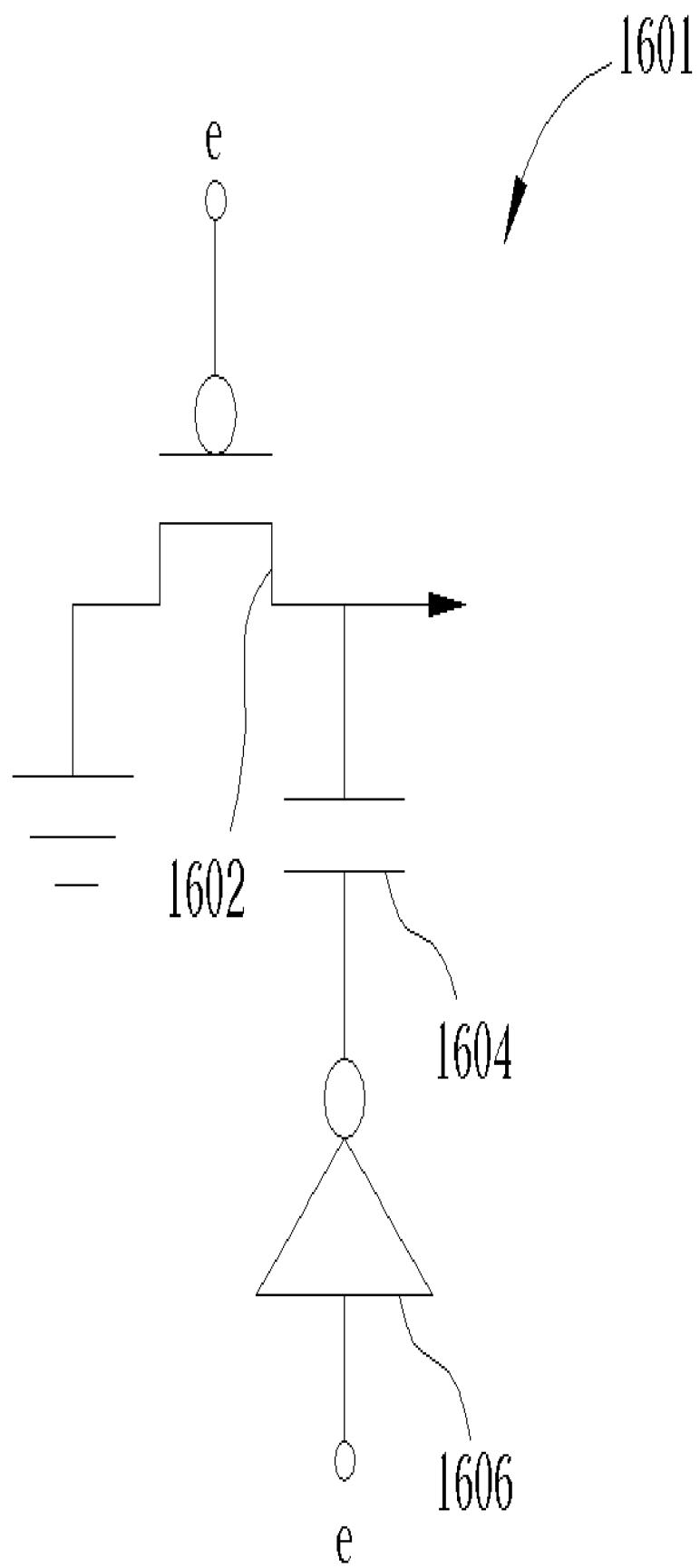


Fig. 16

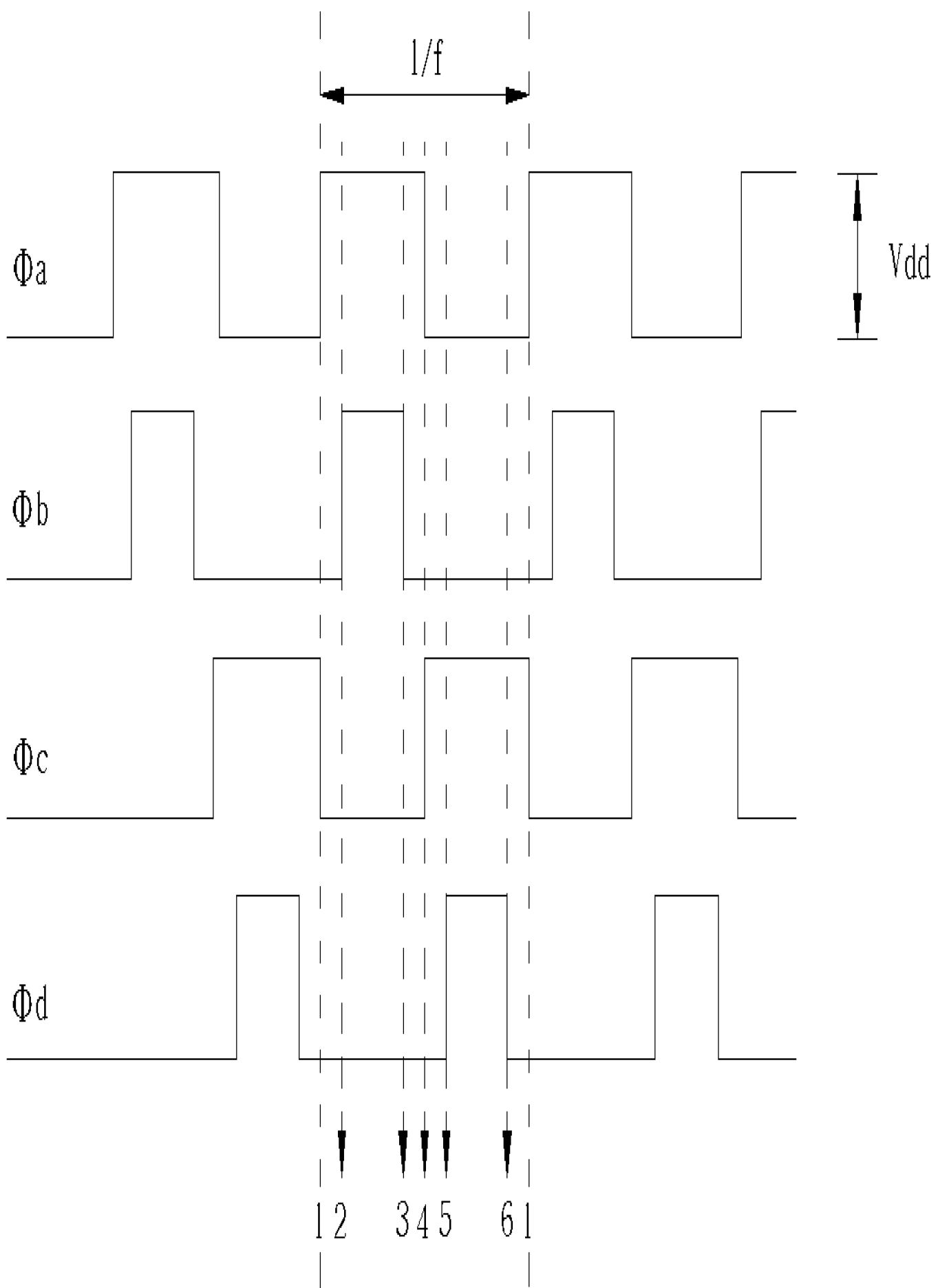
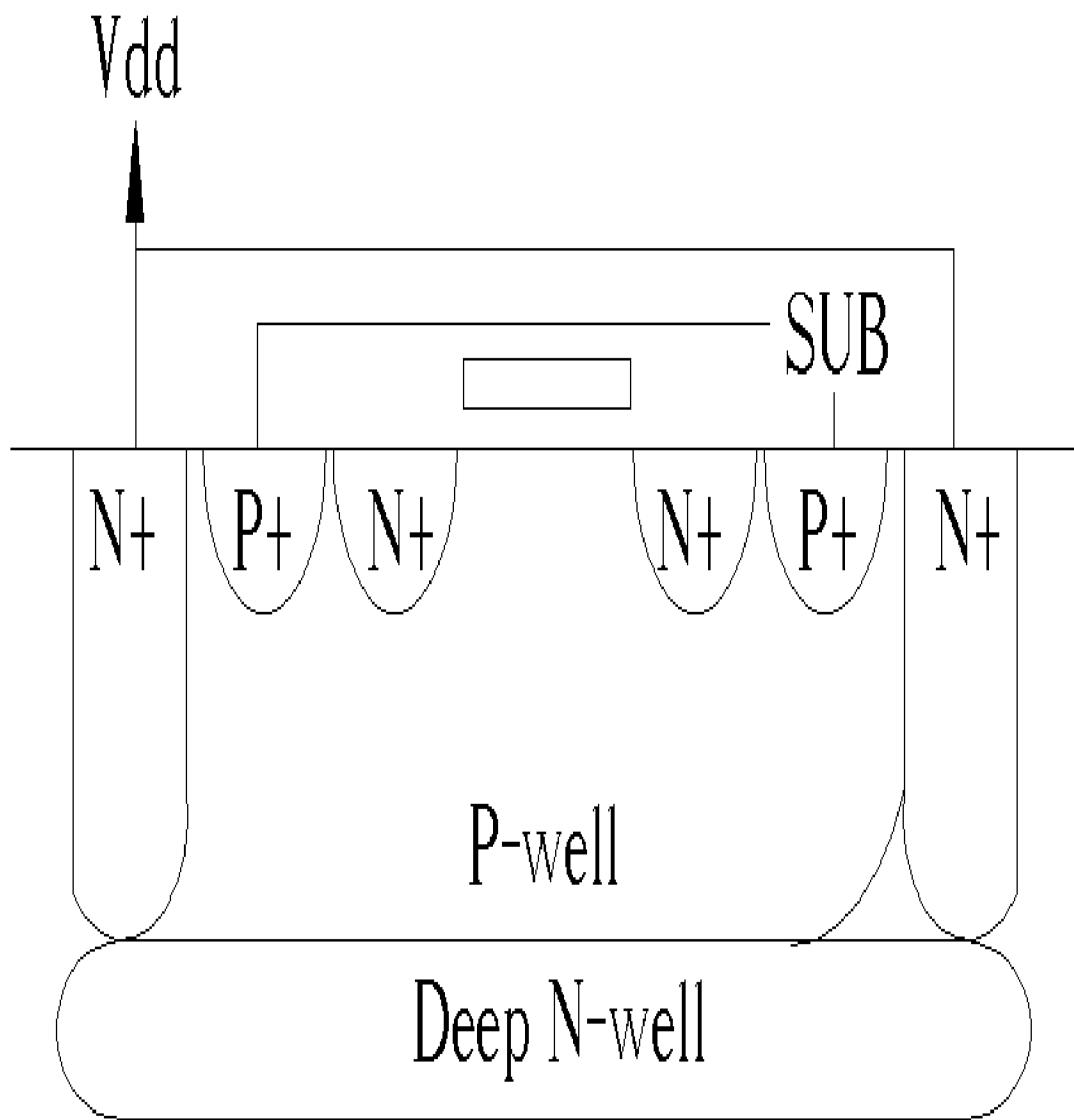


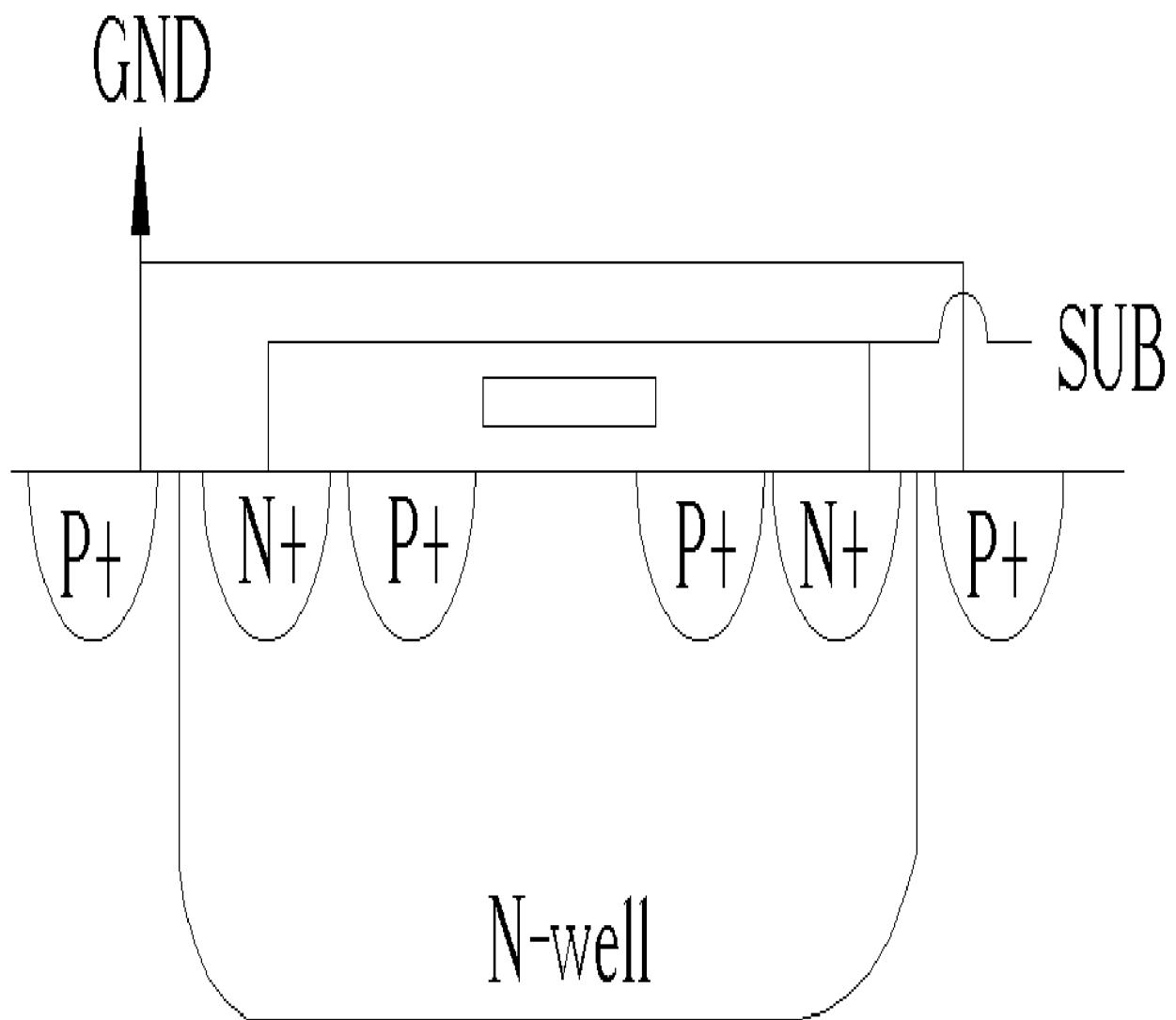
Fig. 17



P-sub

NMOS transistor

Fig. 18



P-sub

PMOS transistor

Fig. 19

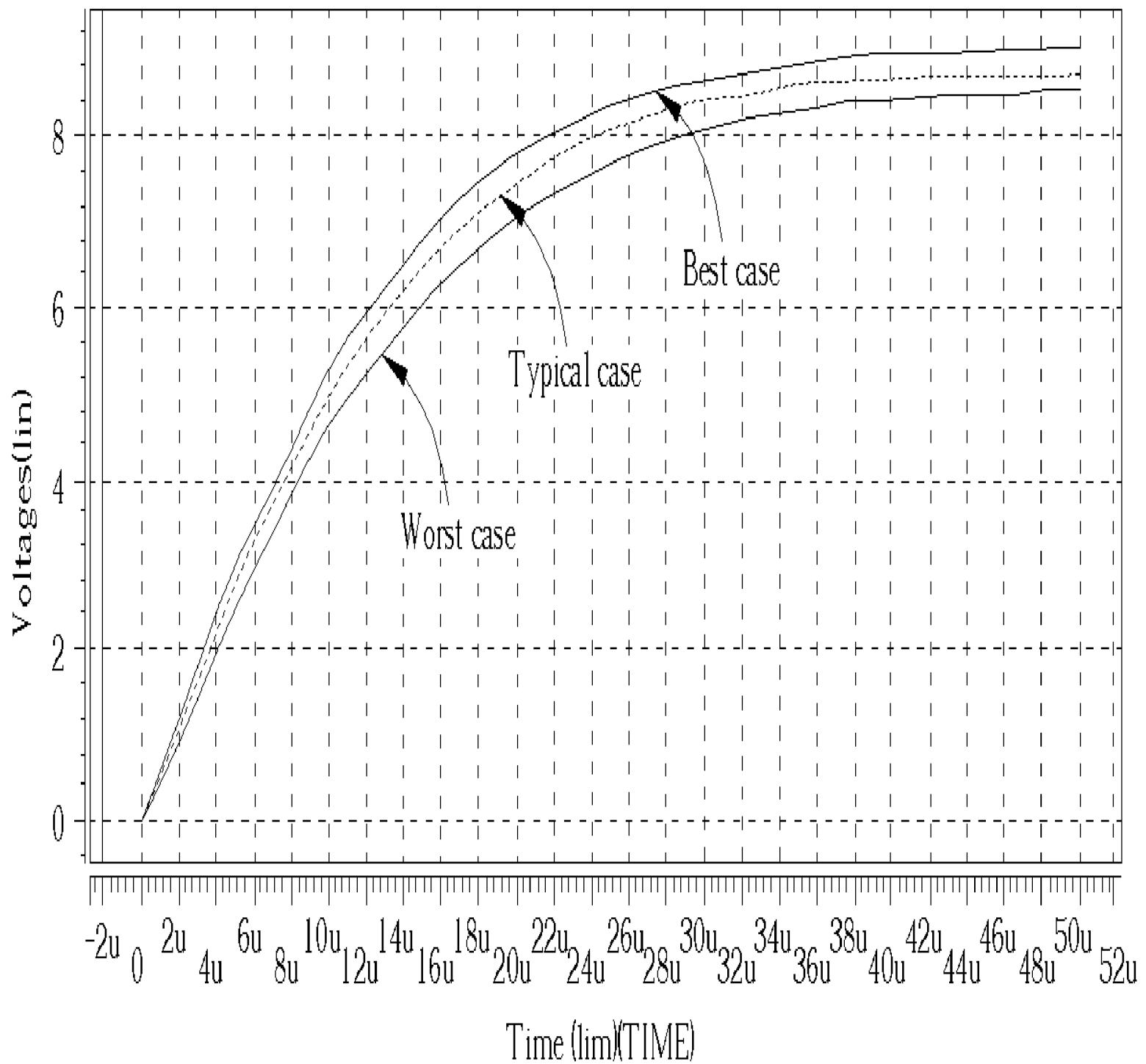


Fig. 20

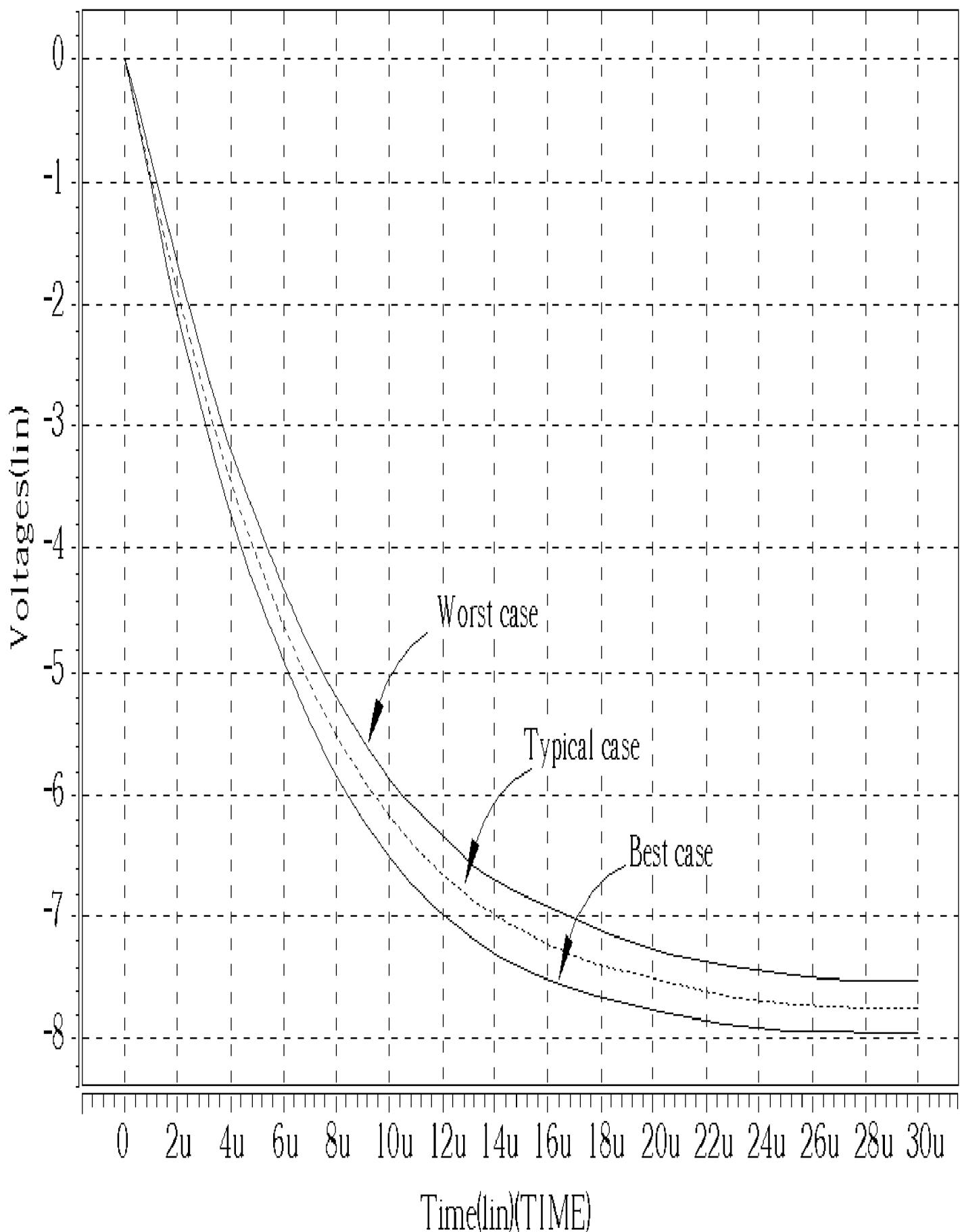


Fig. 21